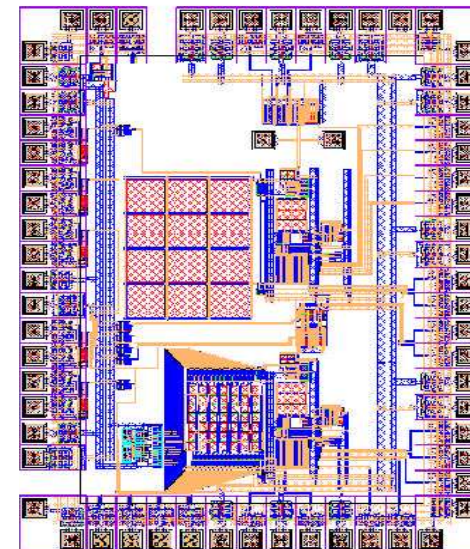
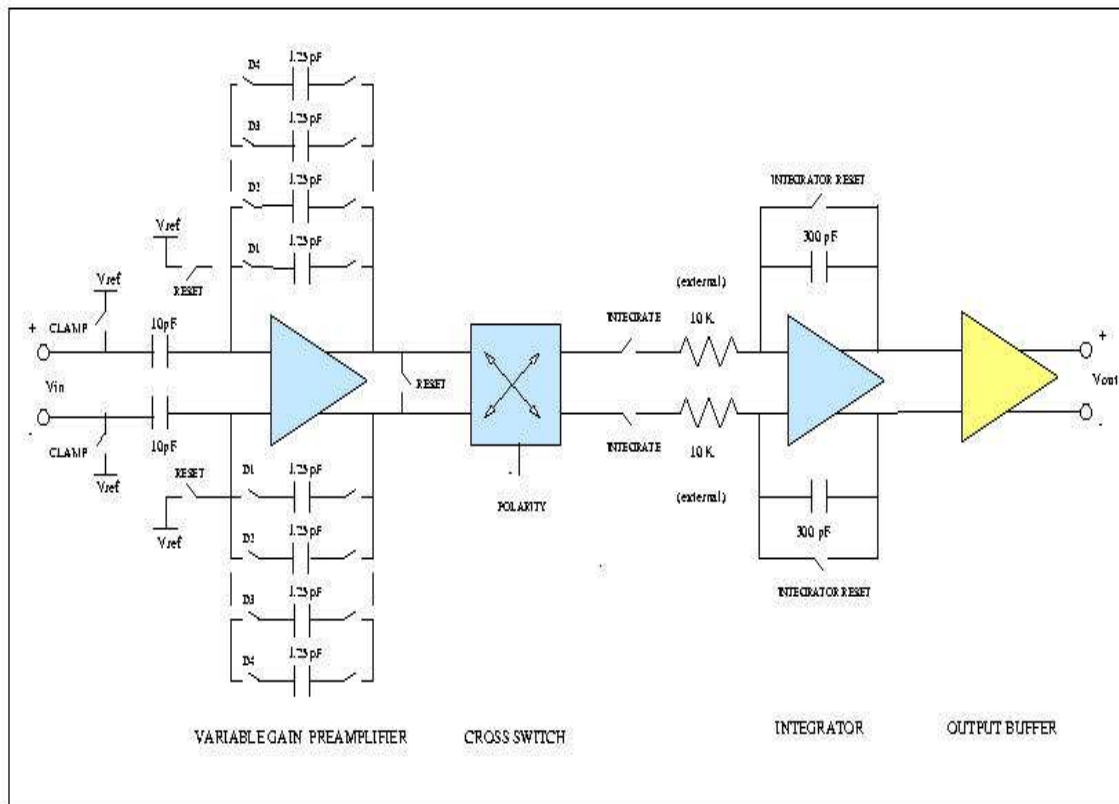


# CDS Prototype Chip Block Diagram and Layout



First prototype version